



EP8260 PowerQUICC II Computing Engines

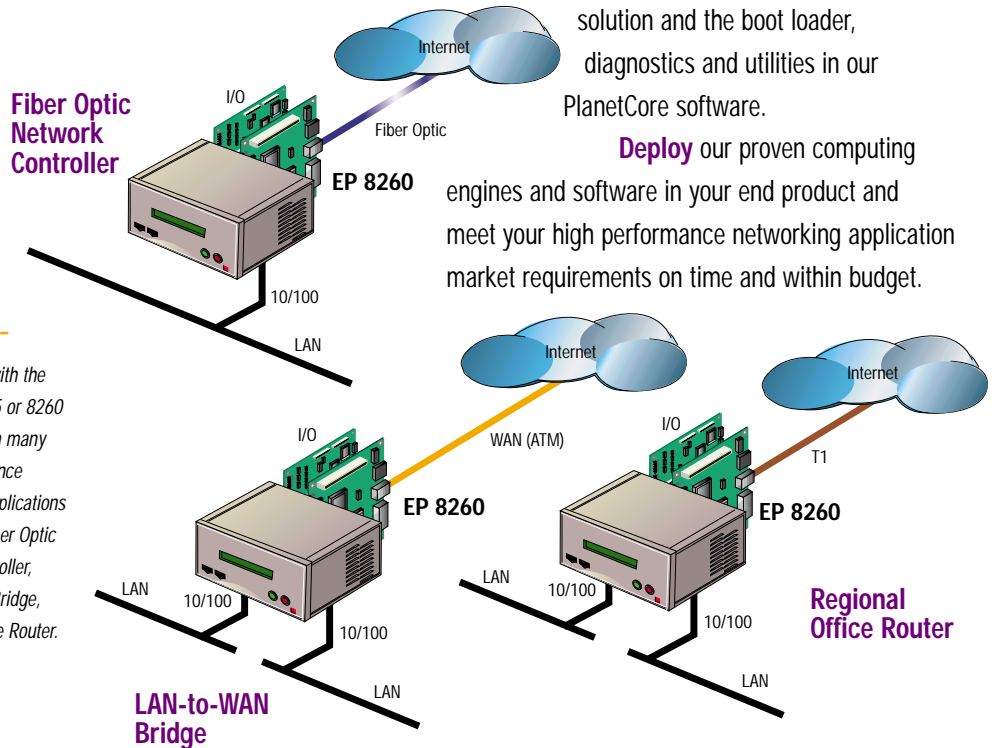


The EP8260 computing engine takes advantage of the high performance networking capabilities of the Motorola PowerPC 8255 or 8260 processors. By integrating all of the features of the PowerQUICC II technology onto one single board, the computing engines reduce Time-To-Market and increase product reliability for complex high-speed networking applications. Scalable in size and flexible in configuration, they provide a production-ready platform for tomorrow's networks.

Design your product leveraging the EP8260 computing engine and the direct bus access to all the 8255 or the 8260 processor features.

Develop prototypes and applications using your preferred RTOS solution and the boot loader, diagnostics and utilities in our PlanetCore software.

Deploy our proven computing engines and software in your end product and meet your high performance networking application market requirements on time and within budget.



The EP8260 with the PowerPC 8255 or 8260 can be used in many high performance networking applications including a Fiber Optic Network Controller, LAN-to-WAN Bridge, Regional Office Router.

design.

The next generation of Internet infrastructure.

develop.

Your products based on our platform.

deploy.

Your solution faster.

Software and hardware working together, helping build the growing Internet Infrastructure.



design.

The next generation of Internet infrastructure.

develop.

Your products based on our platform.

deploy.

Your solution faster.

EP8260 PowerQUICC II computing engines

Design, Develop, Deploy.

The EP8260 is a PC/104 mechanical form factor computing engine. The EP8260 can use all derivatives of the Motorola PowerPC 8255 or 8260 processors. The EP S Bus allows developers direct access to the processor features so the EP8260 can be integrated directly into your design.

Features

- EP8260 accommodates the PowerQUICC II 8255 or 8260 processors
- PC/104 mechanical form factor
- DRAM: Main SDRAM 32, 64 and 128MBytes; Local SDRAM 16, 32 and 64MBytes
- Flash: 4, 8, 16 and 32MBytes
- NVRAM: 0, 32, 128 and 512Kb; RTC
- EP S Bus interface allows all features of the PowerPC bus or CPM bus to be brought out to your product or an I/O module
- Multiple bus mode support: 8260 single bus, 60x bus
- Support for 8260 external masters on expansion cards
- Expansion modules can access the EP S Bus from either side of the computing engine
- Open bus interface with documentation and support provided
- Standard kit includes Ethernet debug cable accessories and Universal (5v) power supply
- CD-ROM with design, reference, and development information
- Support through PlanetTrack (web-based customer service) is available for registered users

Environmental

- Temperature: 0°C to +70°C operating (extended temperature available)
- Altitude: 5000m
- Humidity: 10% to 80%, non-condensing
- Vibration: 2Gs RMS, 20-2000Hz

Peripheral Interfaces

- EP S Bus interface allows all features of the PowerPC bus or CPM bus to be brought out to your product or an I/O module
- 100BaseTX/10 BaseT (FCC3)
- UTOPIA 8, UTOPIA 16, Fast Ethernet supported simultaneously
- Single PCMCIA Type I, II and III
- EIA-232 Serial Monitor Port (SMC1 or 2)
- Debug and JTAG port 1149.1
- Optional board stacking heights: 5,7,9,12 and 19mm

Software Specifications

- PlanetCore: multi-protocol boot-loader, Flash burner. Boots a pre-installed OS from Flash, or loads an OS into RAM through serial or Ethernet ports
- PlanetCore also includes a licensable diagnostics and utilities package for faster application development
- Host environment and support package for MontaVista HardHat Linux and Wind River VxWorks.

Contact Embedded Planet

749 Miner Rd. Cleveland, Ohio 44143

Tel: 440.646.0077

Fax: 440.461.4329

info@embeddedplanet.com

Copyright 2001 Embedded Planet. All rights reserved. Embedded Planet is a registered trademark. Other company and product names may be the trademarks of their respective owners.
Rev. 4/01

*Software and hardware working together,
helping build the growing Internet Infrastructure.*

